VS-GB100TP120N

Vishay Semiconductors





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INT-A-PAK

VCES 1200 V Ic at Tc = 80 °C 100 A VCE(on) (typical) at Ic = 100 A, 25 °C 1.80 V Speed 8 kHz to 30 kHz Package INT-A-PAK Circuit Half bridge

FEATURES

- High short circuit capability, self limiting to $6 \times I_C$
- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Maximum junction temperature 150 °C
- Low inductance case
- Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- AC inverter drives
- Switching mode power supplies
- Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25 \ ^{\circ}C$ unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Collector to emitter voltage	V _{CES}		1200	- V		
Gate to emitter voltage	V _{GES}		± 20	V		
Collector current	1	T _C = 25 °C	200			
	I _C	T _C = 80 °C	100]		
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	200	А		
Diode continuous forward current	I _F		100]		
Diode maximum forward current	I _{FM}		200]		
Maximum power dissipation	PD	T _J = 150 °C	650	W		
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs		
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V		
l ² t-value, diode	l ² t	$V_{R} = 0 V, t = 10 ms, T_{J} = 125 \ ^{\circ}C$	1050	A ² s		

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature.

IGBT ELECTRICAL SPECIFICATIONS ($T_c = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	V_{GE} = 0 V, I_C = 1.0 mA, T_J = 25 °C	1200	-	-	
Collector to emitter voltage	V _{CE(on)}	V_{GE} = 15 V, I_C = 100 A, T_J = 25 °C	-	1.80	2.20	v
		V_{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	2.05	-	
Gate to emitter threshold voltage	V _{GE(th)}	V_{CE} = V_{GE} , I_C = 4.0 mA, T_J = 25 °C	5.0	6.2	7.0	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_J = 25$ °C	-	-	5.0	mA
Gate to emitter leakage current	I _{GES}	V_{GE} = V_{GES} , V_{CE} = 0 V, T_J = 25 °C	-	-	400	nA

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1



RoHS COMPLIANT





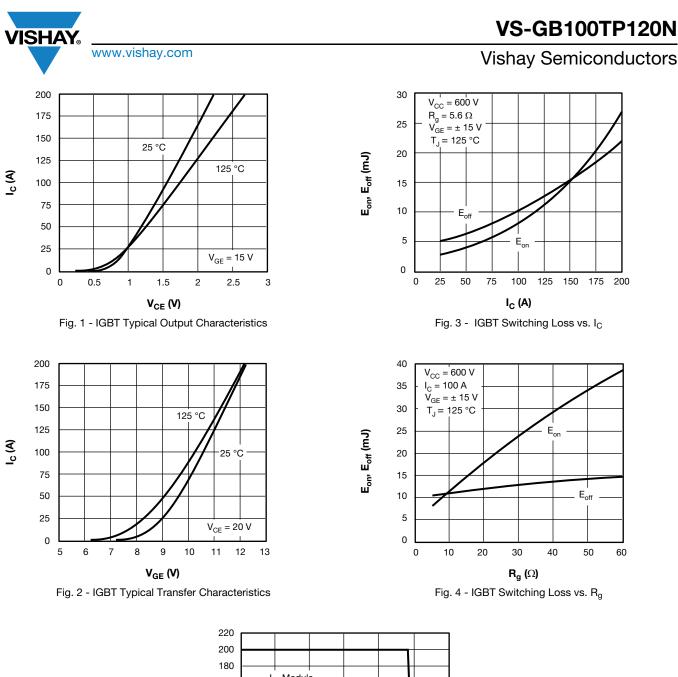
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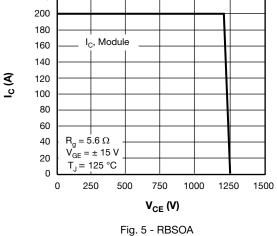
SWITCHING CHARACTERISTICS						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	279	-	ns mJ
Rise time	t _r		-	61	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 100 \text{ A}, \text{ R}_{g} = 5.6 \Omega,$	-	308	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, \text{ T}_{J} = 25 \text{ °C}$	-	205	-	
Turn-on switching loss	E _{on}		-	5.56	-	
Turn-off switching loss	E _{off}		-	6.95	-	
Turn-on delay time	t _{d(on)}		-	287	-	ns
Rise time	tr		-	63	-	
Turn-off delay time	t _{d(off)}	V_{CC} = 600 V, I _C = 100 A, R _g = 5.6 Ω, V _{GE} = ± 15 V, T _J = 125 °C	-	328	-	
Fall time	t _f		-	360	-	
Turn-on switching loss	E _{on}		-	7.85	-	
Turn-off switching loss	E _{off}		-	10.55	-	mJ
Input capacitance	Cies		-	7.43	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 25 V, f = 1.0 MHz, T _J = 25 °C	-	0.52	-	nF
Reverse transfer capacitance	C _{res}	1 1 2 3 6	-	0.34	-	1
SC data	I _{SC}	$\begin{array}{l} t_{sc} \leq 10 \; \mu s, V_{GE} = 15 \; V, T_{J} = 125 \; ^{\circ}C, \\ V_{CC} = 900 \; V, V_{CEM} \leq 1200 \; V \end{array}$	-	470	-	А
Internal gate resistance	R _{gint}		-	2	-	Ω
Stray inductance	L _{CE}		-	-	30	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.75	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS ($T_c = 25$ °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diada forward valtage	V _F	I _F = 100 A	T _J = 25 °C	-	1.90	2.30	V
Diode forward voltage			T _J = 125 °C	-	2.00	-	
Diode reverse recovery charge	Q _{rr}	$I_{F} = 100 \text{ A}, V_{R} = 600 \text{ V}, \\ dI_{F}/dt = -2000 \text{ A}/\mu\text{s}, \\ V_{GE} = -15 \text{ V}$	T _J = 25 °C	-	5.52	-	μC
			T _J = 125 °C	-	11.88	-	
Diode peak reverse recovery current	I _{rr}		T _J = 25 °C	-	85	-	•
			T _J = 125 °C	-	103	-	A
Diode reverse recovery energy	E _{rec}		T _J = 25 °C	-	2.06	-	ml
			T _J = 125 °C	-	5.56	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Operating junction temperature	TJ		-	-	150	°C	
Storage temperature range	T _{STG}		-40	-	125		
Junction to caseIGBT (per 1/2 module)	Б		-	-	0.19		
Diode (per 1/2 module)	R _{thJC}		-	-	0.28	K/W	
Case to sink	R _{thCS}	Conductive grease applied	-	0.05	-		
Mounting torque	Power terminal screw: M5		2.5 to 5.0			Nm	
Mounting torque		Mounting screw: M6	3.0 to 5.0		INIT		
Weight of module				150		g	

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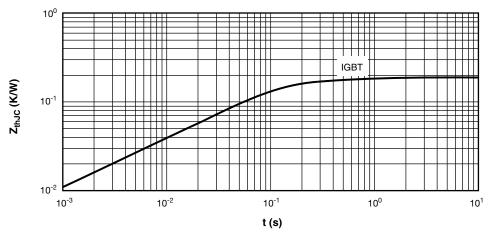
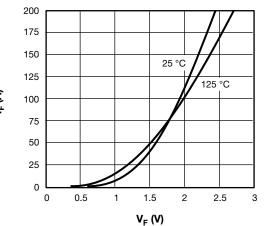


Fig. 6 - IGBT Transient Thermal Impedance



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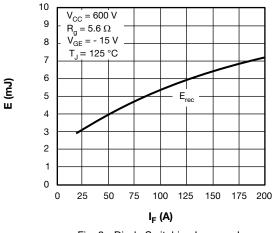


Fig. 8 - Diode Switching Loss vs. I_C

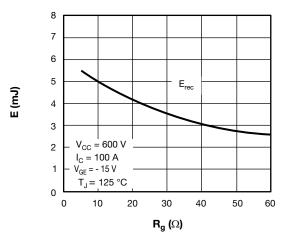


Fig. 9 - Diode Switching Loss vs. Rg

I_F (A)

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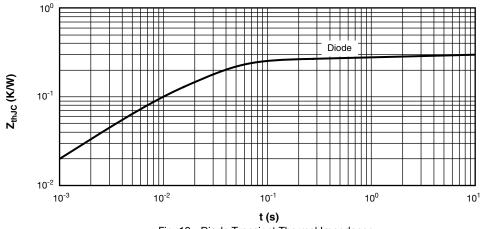
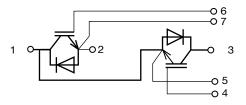


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION

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LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95524				



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